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LSI LOGIC CORPORATION
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1621 Barber Lane

M/S D-106

Milpitas, CA 95035 Fax: (408) 433-7460

Date:

September 22, 2005

To

Name:

USPTO - MS: IDS

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571-273-8300

Phone:

703-305-8283

From:

Name:

Manu Kashyap, Intellectual Property Paralegal

Corporate Legal Dept.

Telephone:

(408) 433-7475

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(408) 433-7460

Ře:

10/634,634

Number of Pages Including this Page

19

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#### MESSSAGE:

Docket Number: 03-0339

Box: ID\$

Group Art: 2825

Examiner: Naum B. Levin

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		- 1	Application Number	10/634,634
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(to be used for all ca	ORM	er initial filing)	First Named Invent	or Christopher Hamlin
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	<u></u>	EN	CLOSURES (check all	that apply)
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ndividual Name			ICANT, ATTORNEY, lo. 36,771, Phon	e: [+1] 408-433-7475
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		CERTIF	ICATE OF MAILING	
I hereby certify that this Assistant Commissione	correspondence is be r for Patents, Washing	ing deposited with to gron, D.C. 20231 or	the United States Postal Sen	ice as first class mail in an envelope to:
Typed or printed name	Manu Kas	hyap, Phoi	ne: [+1] 408 <b>-4</b> 33	
Signature		6_	2	Date 9/2 2/65
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<ul> <li>Any comments on the Washington, DC 202 Patents, Washington</li> </ul>	31. DO NOISENDI	FEES OR COMPLE	TED FORMS TO THIS ADD	RESS. SEND TO : Assistant Commissioner for

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Application Number

4084337460 LSI LOGIC → 915712738300

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Complete if Known

10/634,634

August-04, 2003

Christopher Hamlin

Patent Fees are subject to annual revision on October 1. These are the fees effective October 1, 1997.	Firs	First Named Inventor			Christopher Hamlin		
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Typed or printed name Timothy Croll			·			36,771	
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Patent Fees are subject to annual revision on October 1. These are the fees effective October 1, 1997. These properties must be supported by a small stilly statement,	Filing Date	August-04, 2003				
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erwise large entity fees must be paid. Sec Forms PTO/SB/09-12.	Evaminer Name	Naum B. Levin				

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otherwise large See 37 C.F.R. 1,27 and 1,28. AMOUNT OF PAYMENT

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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§

In re Application of:

Christopher Hamlin

Serial No.:

10/634,634

Filed:

August 04, 2003

For:

Method and Apparatus for Mapping Platform-Based Design to Multiple

Foundry Processes

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Group Art Unit:

2825

SEP 2 2 2005

Examiner:

Levin, Naum B.

**Atty Docket:** 

/ 03-0339

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Manu Kashyap

9/22/25

Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 22 Sept 05

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Respectfully submitted,

Timothy Croll

Reg. No. 36,771

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STATEMENT BY APPLICANT			PLICANT	First Named Inventor	Christopher Hamlin				
				Group Art Unit	2825				
				Examiner Name	Naum B. Levin				
Sheet	1	of	1	Attorney Docket No.	/ 03-0339				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Çite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where				
		Mosis Scalable CMOS (SCMOS) design rules (Revision 8.0) updated 4/25/2003; http://www.mosis.org/technical/designrules/scmos/scmos-main.html; 4/30/03. Pgs 1-14				

Date Examiner considered signature